**4 Oct** – Thesis Report Due

**7-13 Oct** – VIVA Presentation

Having working breadboard model (possibly off solar) with battery voltage and solar panel current checks operational.

**14-20 Oct** – DSP and Comms test

**21-27 Oc** – P&M and comms reports

**28 Oct – 3 Nov** – Exam study week

Have solar panels, energy storage and voltage regulation researched and selected

**4-10 Nov** – Exam week

**11-24 Nov** – LC2

**25 Nov – 16 Feb** – Holidays

All sub-systems designed, and components selected

Initial PCB systems designed and constructed

PCB tested with silicon solar panels

Initial software developed

Basic ground station receiver constructed (no tracking)

Full PCB design tested during the semester

**17-23 Feb** – CDF week

**24 Feb – 1 Mar** – Week 1

**2 – 8 Mar** – Week 2

**9 – 15 Mar** – Week 3

**16 – 22 Mar** – Week 4

**23 – 29 Mar** – Week 5

**30 Mar – 5 Apr** – Week 6

**6-19 Apr** – Mid-semester break

**20-26 Apr** – Week 7

**27 Apr – 3 May** – Week 8

**4 -10 May** – Week 9

**11-17 May** – Week 10

**18-24 May** – Week 11 – draft summary report (24 May)

**25 -31 May** – Week 12

**1-7 Jun** – Week 13

**8-14 Jun** – Week 14 – final summary report (11 Jun)

**15-21 Jun** – Revision week – Project deliverables (17 Jun)